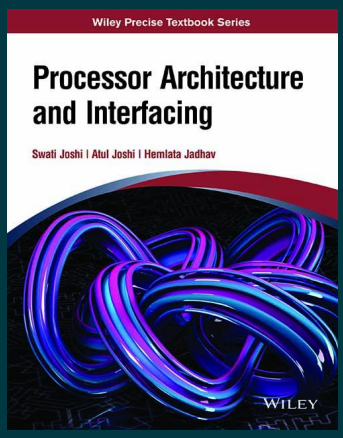
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By Swati Joshi, Atul Joshi, Hemlata Jadhav

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Description

The objective of the book Processor Architecture and Interfacing is to study the operation, performance, and applications of the microprocessors and microcontrollers; mainly the 80386 microprocessor and 8051 microcontroller. It provides insights into the 80386 and 8051 chips for their use in practical applications. The book is enriched with detailed illustrations and worked-out examples.

About the Author

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